

MODIFICATION AVAILABLE – PERFORMANCE ENHANCEMENT  
CHARGEABLE TO CUSTOMER SERVICE / RELIABILITY  
ENHANCEMENT CHARGEABLE TO CONTRACT IF THERE IS ONE.

**1661C-03A**

# S E R V I C E N O T E

Supersedes:  
NONE

1661C

Serial Numbers: [0000A00000/9999Z99999]

**Error in the Threshold Accuracy Test Procedure.**

**To Be Performed By: Agilent-Qualified Personnel.**

**Parts Required:**

P/N	Description	Qty.
NONE		

## ADMINISTRATIVE INFORMATION

SERVICE NOTE CLASSIFICATION:		
<b>MODIFICATION AVAILABLE</b>		
ACTION CATEGORY:	AGREEABLE TIME	<input type="checkbox"/> PERFORMANCE ENHANCEMENT <input checked="" type="checkbox"/> SERVICE / RELIABILITY ENHANCEMENT
LOCATION CATEGORY:	<input type="checkbox"/> CUSTOMER INSTALLABLE <input type="checkbox"/> ON-SITE <input checked="" type="checkbox"/> SERVICE CENTER	AVAILABLE UNTIL: N/A
AUTHOR: ABR    PRODUCT LINE: 1A		
ADDITIONAL INFORMATION:		

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June 6, 2002

**Situation:**

Pods tested and left at User Level – 0V will cause other pods to fail the Threshold Accuracy Test.

**Solution/Action:**

This procedure is in the Service Guide for the Logic Analyzers stated above.

When testing Pods 1 and 2, they are set to ECL and then User-0V. Then, when testing Pods 3 and 4, Pods 1 and 2 are left at User-0V. This is where the problem occurs. Pod 1 is left at 0V and is left floating, or acquiring noise. This affects Pod 3, and causes it to fail the test. So, after testing each pod, put the level back to TTL. This does not have any affect on customer use of the logic analyzer.